ISOLATION OF I2C BUSES IN A MULTIPLE POWER DOMAIN ENVIRONMENT USING MUTLIPLEXERS

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ABSTRACT

A disk enclosure includes a first plurality of I2C elements powered by a first power domain and a second plurality of I2C devices powered by a second power domain. The first plurality of I2C elements are coupled via a first I2C bus and the second plurality of I2C elements are coupled via a second I2C bus. An enclosure controller has a third I2C bus selectively coupled via a multiplexer to the first or the second I2C bus.